Dr. Chen, Hsin was born in 1974 in Taiwan. He received his B.S. and M.S. degrees in Electrical Engineering (EE) in 1996 and 1998, respectively, from the National Tsing Hua University (NTHU), Taiwan. After two years' military service, he did PhD in Electronics at the Edinburgh University, UK during 2000-2004. He is now appointed as an associate professor by the EE Dept. at the NTHU. Hsin's EE expertise lies in analogue and mixed-mode VLSI design. As a researcher, he has led the Neuro-Engineering Lab and devoted his greatest enthusiasm to "neural hardware", looking to explore bio-inspired, particularly brain-inspired, computation in VLSI. The "neural hardware" involves VLSI implementation of probabilistic neural computation, neuromorphic VLSI that mimics the morphology of biological systems, and neuro-engineered VLSI that could "communicate" with biological nervous systems.

Web site : http://www.ee.nthu.edu.tw/hchen